

۲

2018년 2월 5일[월] - 7일[수], 강원도 하이원리조트 컨벤션호텔

## [WI3-K-5] Analysis of LRS retention fail based on Joule heating effect in InGaZnO RRAM

<u>Geumho Ahn</u>, Jun Tae Jang, Daehyun Ko, Hyeri Yu, Haesun Jung, Jihyun Rhee, Hyun-Sun Mo, Sung-Jin Choi, Dong Myong Kim, and Dae Hwan Kim<sup>a)</sup> School of Electrical Engineering, Kookmin University, Seoul 136-702, Korea E-mail : <sup>a)</sup>drlife@kookmin.ac.kr

The resistance switching effects of metal oxide have been recently investigated for applications as digital swithces of due to their low power consumption, simple structure, and high speed [1]. Many metal oxide materials have been reported to have resistive switching characteristics, especially, InGaZnO (IGZO)-based resistive random access memory (RRAM) has successfully demonstrated along with many advantages, such as a good uniformity for large area, room temperature process [2-3]. However, the modelling study on retention characteristics of RRAM has been rarely investigated. So, in this work, the fail of retaining "SET" state caused by Joule heating to IGZO RRAM which has bipolar resistive switching characteristic like Fig. 1(b) is investigated. Joule heating occurs and causes the fail of retaining "SET" state, even though the polarity of voltage is same as that of "SET" bias like Fig. 1(a). To observe this fail, we simply apply DC bias to IGZO RRAM which state is "SET" and the results of DC bias measurement is shown as Fig. 1(c). It is found that there is the threshold of power (Pth) which can cause Joule heating (our model results : Pth of IGZO RRAM ≈ 7.5 mW ~ 9 mW like inset of Fig. 1(c)) and also when the power is over Pth, the oxygen vacancy diffusion caused by the energy of Joule heating is stronger than the formation of oxygen vacancy, finally undesirable reset occurs. That is similiar with the reset mechanism of unipolar resistive switching device [4]. Our results suggest that Joule heating effect of RRAM can cause undesirable fail of retaining "SET" state and also the analysis of this phenomenon seems to be useful for analyzing and modelling the retention characteristics of RRAM devices.



Fig 1. (a) Fail mechanism of retaining "SET" state on IGZO RRAM, (b) structure and electrical characteristics of IGZO RRAM, (c) amplitude of voltage dependent-results of DC bias measurements.

Acknowledgement: This work was supported by the National Research Foundation of Korea (NRF) Grant funded by the Korean Government(MSIP) under Grant 2016R1A5A1012966, in part by the Ministry of Science, ICT and Future Planning Grant 2016M3A7B4909668, and in part by the Ministry of Education, Science and Technology(MEST) Grant 2017R1A2B4006982.

References: [1] J. Borghetti, et al., Nature, 2010. [2] C.H. Kim, et al., Appl. Phys. Lett., 2010. [3] M.-C. Chen, et al., ECS Electrochemical and Solid-State Letters, 2010. [4] Q. Xia, et al., Nano Lett., 2009.